

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Sheet 1 of 2

Substitute for form 1449/PTO

Complete if Known	
Application Number	10/053,340
Filing Date	10/22/2001
First Inventor	WARE, Frederick
Art Unit	2187
Examiner	Nguyen, Than
Atty Docket #	RBS2.P049
Rambus #	RA207.CPI.US

US Patent Documents

Examiner Initials*	Cite No.	Document No.	Date (MM-DD-YYYY)	Patentee	Class	Subclass
0000040	4656605	04/07/1987	Clayton	365	52	
0000262	5606717	02/25/1997	Farmwald, et al.	710	36	
0000767	5742798	04/21/1998	Goldrian	395	551	
0000837	2001-0047450	11/29/2001	Gillingham et al.	711	105	
0001080	5867541	02/02/1999	Tanaka et al.	375	354	
0001083	6336205	01/01/2002	Kurokawa et al.	716	2	
0001297	5796624	08/18/1998	Sridhar et al.	364	489	
0001749	6449727	09/10/2002	Toda	713	401	
0001817	6226723	05/01/2001	Gustavson et al.	711	170	
0002073	6016282	01/18/2000	Keeth	365	233	
0002293	6539454	03/25/2003	Mes	711	105	
0002443	5778419	07/07/1998	Hansen et al.	711	112	
0002469	6839393	01/04/2005	Sidiropoulos	375	371	
0002632	4280221	07/21/1981	Chun et al.	375	17	
0002689	5475690	12/12/1995	Burns et al.	370	105.3	
0005252	2002-0174311	11/21/2002	Ware, et al.	711	167	
0006169	2004-0054845	03/18/2004	Ware, et al.	711	100	
0006183	6356260	03/12/2002	Montalbo	345	204	
0006184	6618736	09/09/2003	Menage	707	204	
0006185	6760857	07/06/2004	Lau, et al.	713	500	
0006529	7095661	08/22/2006	Osaka , et al.	365	194	
0006530	6504790	01/07/2003	Wolford	365	233	
0006863	3820081	06/25/1974	Donahue	360	172.5	
0006864	5329484	07/12/1994	Tsuiki	365	45	
0007113	6445624	09/03/2002	Janzen et al.	365	191	
0007114	6724666	04/20/2004	Janzen et al.	365	191	
0007726	7251162	07/31/2007	Kawajiri et al.	365	185.18	
0007727	7057948	06/06/2006	Shimizu et al.	365	200	
0007931	6075393	06/13/2000	Tomita et al.	327	153	

Foreign Patent Documents

Examiner Initials*	Cite No.	Cty	Document No.	Date (MM-DD-YYYY)	Patentee	Class	Subclass	T**
0003534	JP		2000035831	02/02/2000	Nippon Electric Co.	G06F	1/10	
0007642	JP		2000348490	12/15/2000	Hitachi Ltd.	G11C	11/413	
0007643	JP		11284126	10/15/1999	Fujitsu Ltd.	H01L	25/16	
0007644	WO		9815897	04/16/1998	Hewlett-Packard Company	G06F	13/16	
0007645	JP		2000174505	06/23/2000	Hitachi Ltd.	H01P	1/26	
0007969	JP		2008227394	09/03/1996	Monolithic System Technology, Inc.	G06F	13/40	

Examiner Signature:	/Than Nguyen/	Date Considered:	08/22/2008
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. **Applicant is to place a check mark here if English language Translation is attached.

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Examiner Initials*	Cite No.	Cty	Document No.	Date (MM-DD-YYYY)	Patentee	Class	Subclass	T**
	0007970	JP	2001027918 A	01/30/2001	Hitachi Ltd.	G06F	3/00	

Non-Patent Literature Documents

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T**
	0003940	RAMBUS INC. Data Sheet, "8/9-Mbit (1M x 8/9) and 16/18-Mbit (2M x 8/9) RDRAM", March 1996, pages 1-30	
	0003966	RAMBUS INC., Rambus RIMM Module (with 64/72Mb RDRAMs) Data Sheet, Preliminary Information, Document DL0078 Version 0.90, March 1999, pages I - 11	
	0004073	RAMBUS, INC. Direct Rambus Short Channel Layout Guide, Version 0.8, March 2000, 34 pages	
	0004320	"Standard for a High-Speed Memory Interface (SyncLink)," IEEE Draft 0.99 IEEE P 1596.7.199X, 1996, 66 pages.	
	0004474	THE INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS: "Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface Signalling Technology (RamLink)" 19 March 1996 (1996-03-19), IEEE STD 1596.4-1996, XP002315223 * pages 12,43-45 *	
	0006661	International Preliminary Report on Patentability and Written Opinion of the International Searching Authority in International Application PCT/US2005/032346, European Patent Office, March 29, 2007, 10 pages.	
	0007023	International Preliminary Report and Written Opinion of the International Searching Authority in International Application PCT/US/2005/032770, World Intellectual Property Organization, April 12, 2007, 6 pages. (RBS2.P034WO)	
	0007024	International Search Report and Written Opinion of the International Searching Authority in International Application PCT/US2005/042722, World Intellectual Property Organization, May 10, 2006, 15 pages. (RBS2.P011WO)	
	0007597	YEUNG et al., 2000, "A 2.4 Gb/s/pin Simultaneous Bidirectional Parallel Link with Per-Pin Skew Compensation," IEEE Journal of Solid-State Circuits, Vol. 35, No. 11, Pages 1619-1627	
	0008004	Notice of Opposition to European Patent No. 1291778; Application No. 02009032.0; 24 pages	

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